

2SC5077, 2SC5077A

Silicon NPN triple diffusion planar type

For high breakdown voltage high-speed switching

■ Features

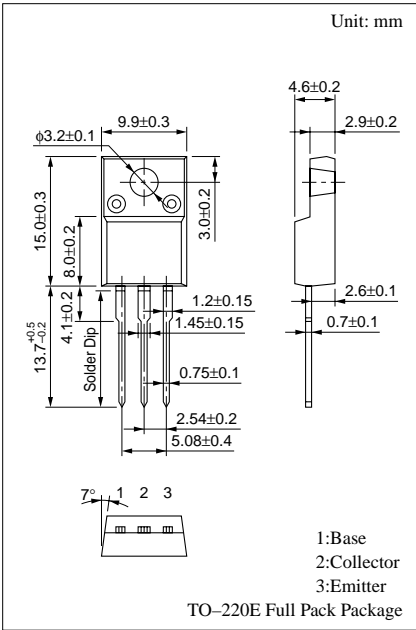
- High-speed switching
- High collector to base voltage V_{CBO}
- Wide area of safe operation (ASO)
- Satisfactory linearity of forward current transfer ratio h_{FE}
- Full-pack package with outstanding insulation, which can be installed to the heat sink with one screw

■ Absolute Maximum Ratings ($T_C=25^{\circ}C$)

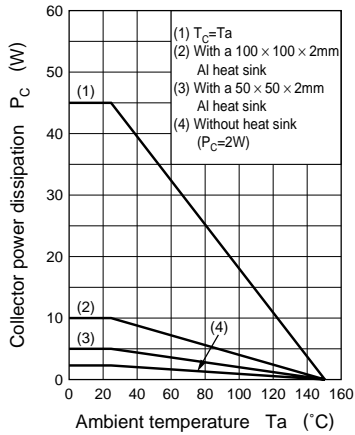
Parameter		Symbol	Ratings	Unit
Collector to base voltage	2SC5077	V_{CBO}	800	V
	2SC5077A		900	
Collector to emitter voltage	2SC5077	V_{CES}	800	V
	2SC5077A		900	
Collector to emitter voltage		V_{CEO}	500	V
Emitter to base voltage		V_{EBO}	8	V
Peak collector current		I_{CP}	15	A
Collector current		I_C	7	A
Base current		I_B	4	A
Collector power dissipation	$T_C=25^{\circ}C$	P_C	45	W
	$T_a=25^{\circ}C$		2	
Junction temperature		T_j	150	$^{\circ}C$
Storage temperature		T_{stg}	-55 to +150	$^{\circ}C$

■ Electrical Characteristics ($T_C=25^{\circ}C$)

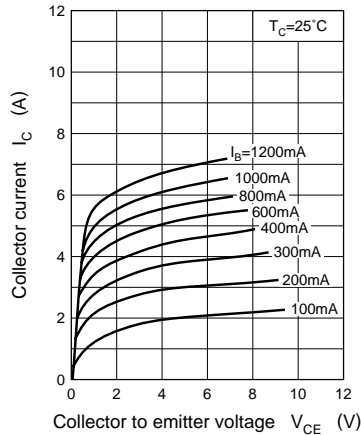
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 800V, I_E = 0$			100	μA
2SC5077A		$V_{CB} = 900V, I_E = 0$			100	
Emitter cutoff current	I_{EBO}	$V_{EB} = 5V, I_C = 0$			100	μA
Collector to emitter voltage	V_{CEO}	$I_C = 10mA, I_B = 0$	500			μA
Forward current transfer ratio	h_{FE1}	$V_{CE} = 5V, I_C = 0.1A$	15			V
	h_{FE2}	$V_{CE} = 5V, I_C = 4A$	8			
Collector to emitter saturation voltage	$V_{CE(sat)}$	$I_C = 4A, I_B = 0.8A$			1.0	V
Base to emitter saturation voltage	$V_{BE(sat)}$	$I_C = 4A, I_B = 0.8A$			1.5	V
Transition frequency	f_T	$V_{CE} = 10V, I_C = 0.5A, f = 1MHz$			1.0	MHz
Turn-on time	t_{on}	$I_C = 4A, I_{B1} = 0.8A, I_{B2} = -1.6A, V_{CC} = 200V$			1.0	μs
Storage time	t_{stg}				3.0	μs
Fall time	t_f				0.3	μs



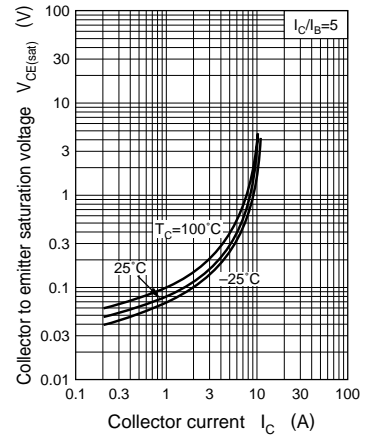
$P_C - T_a$



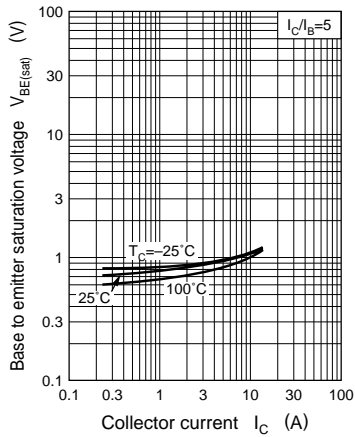
$I_C - V_{CE}$



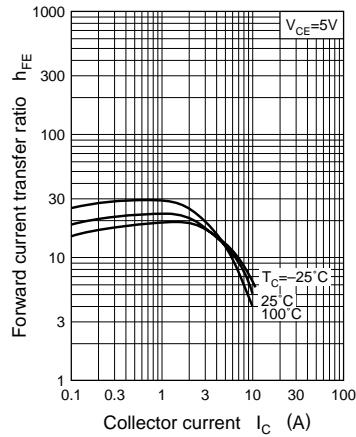
$V_{CE(sat)} - I_C$



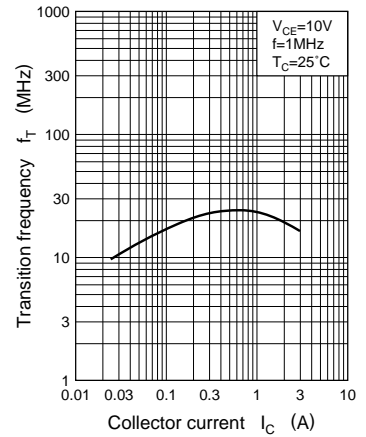
$V_{BE(sat)} - I_C$



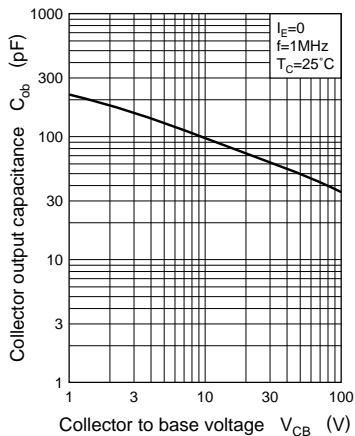
$h_{FE} - I_C$



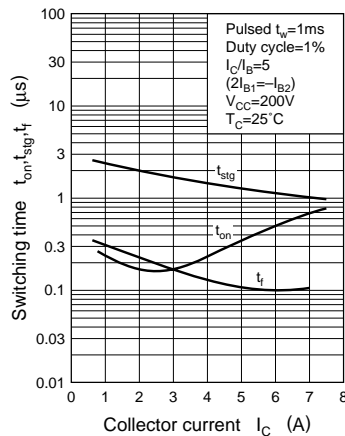
$f_T - I_C$



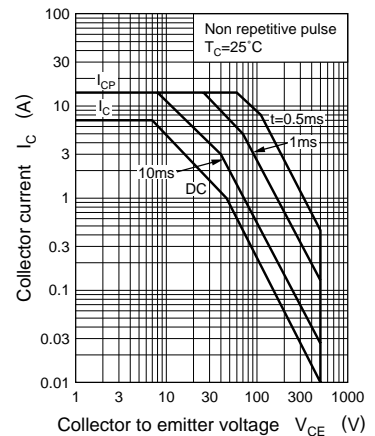
$C_{ob} - V_{CB}$



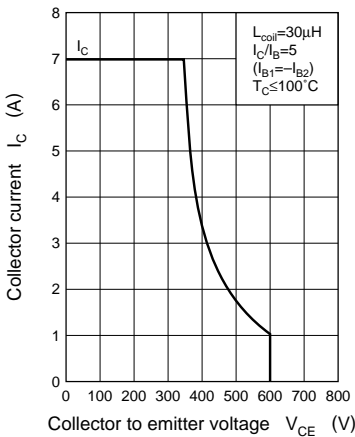
$t_{on}, t_{stg}, t_f - I_C$



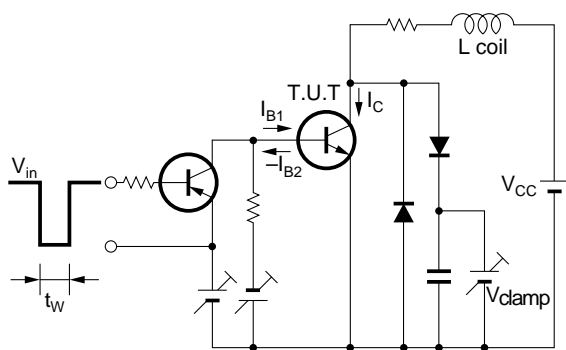
Area of safe operation (ASO)



Area of safe operation, reverse bias ASO



Reverse bias ASO measuring circuit



$R_{th(t)} - t$

